國立宜蘭大學96學年度轉學招生考試

(考生填寫) 准考證號碼:

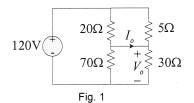
電 路 學 試 題

《作答注意事項》

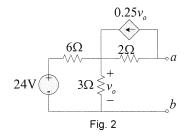
- 1. 請先檢查准考證號碼、座位號碼及答案卷號碼是否相符。
- 2. 考試時間:80 分鐘。
- 3. 本試卷共有 8 大題,各題配分均標示於題目後,共計 100 分。
- 4. 請將答案寫在答案卷上;無推導計算過程及單位者不計分。(請用黑、藍原子筆作答)
- 5. 考試中禁止使用大哥大或其他通信設備。
- 6. 考試後,請將試題卷及答案卷一併繳交。
- 7. 本試卷採雙面影印,請勿漏答。

電路學考科

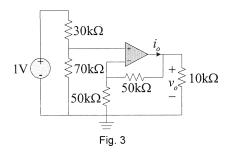
1. For the circuit shown in Fig. 1:



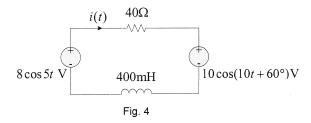
- (a) Find current I_o and voltage V_o . (10%)
- (b) Calculate the power dissipated in the 5Ω resistor. (5%)
- 2. Find the Norton equivalent circuit at terminals a-b of the circuit in Fig. 2. (10%)



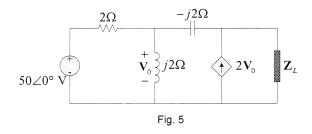
3. Find v_o and i_o in the circuit of Fig. 3. Assume the OPA is ideal. (10%)



4. Using the superposition to find i(t) in the circuit of Fig. 4 under steady state. (15%)



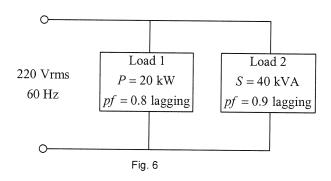
5. It is desired to transfer maximum power to the load \mathbf{Z}_L in the circuit of Fig. 5, find \mathbf{Z}_L and the maximum power. (15%)



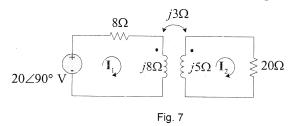
96 學年度轉學招生考試入學 **電路學**考科

第2頁,共2頁

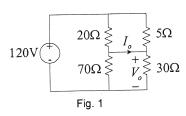
- 6. A 220Vrms, 60Hz source supplies two loads connected in parallel as shown in Fig. 6.
 - (a) Find the total complex power and power factor of the parallel combination. (10%)
 - (b) Calculate the value of the capacitance connected in parallel that will raise the power factor to 0.95. (5%)



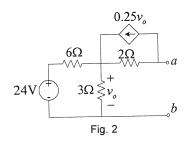
- 7. A balanced delta-connected load has a phase current $I_{AC} = 20 \angle -25^{\circ} A$.
 - (a) Determine the three line currents assuming that the circuit operates in the positive phase sequence. (5%)
 - (b) Calculate the load impedance if the line voltage is $V_{AB} = 120 \angle 0^{\circ} \text{ V}$. (5%)
- 8. Calculate the phasor currents I_1 and I_2 in the circuit of Fig. 7. (10%)



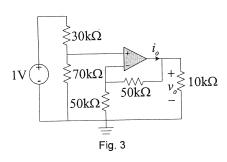
1. For the circuit shown in Fig. 1:



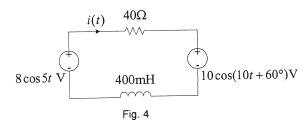
- (a) Find current I_o and voltage V_o . (10%)
- (b) Calculate the power dissipated in the 5Ω resistor. (5%)
- 2. Find the Norton equivalent circuit at terminals a-b of the circuit in Fig. 2. (10%)



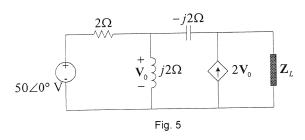
3. Find v_o and i_o in the circuit of Fig. 3. Assume the OPA is ideal. (10%)



4. Using the superposition to find i(t) in the circuit of Fig. 4 under steady state. (15%)



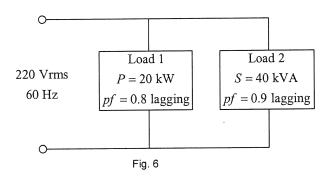
5. It is desired to transfer maximum power to the load \mathbf{Z}_L in the circuit of Fig. 5, find \mathbf{Z}_L and the maximum power. (15%)



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第2頁,共2頁

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